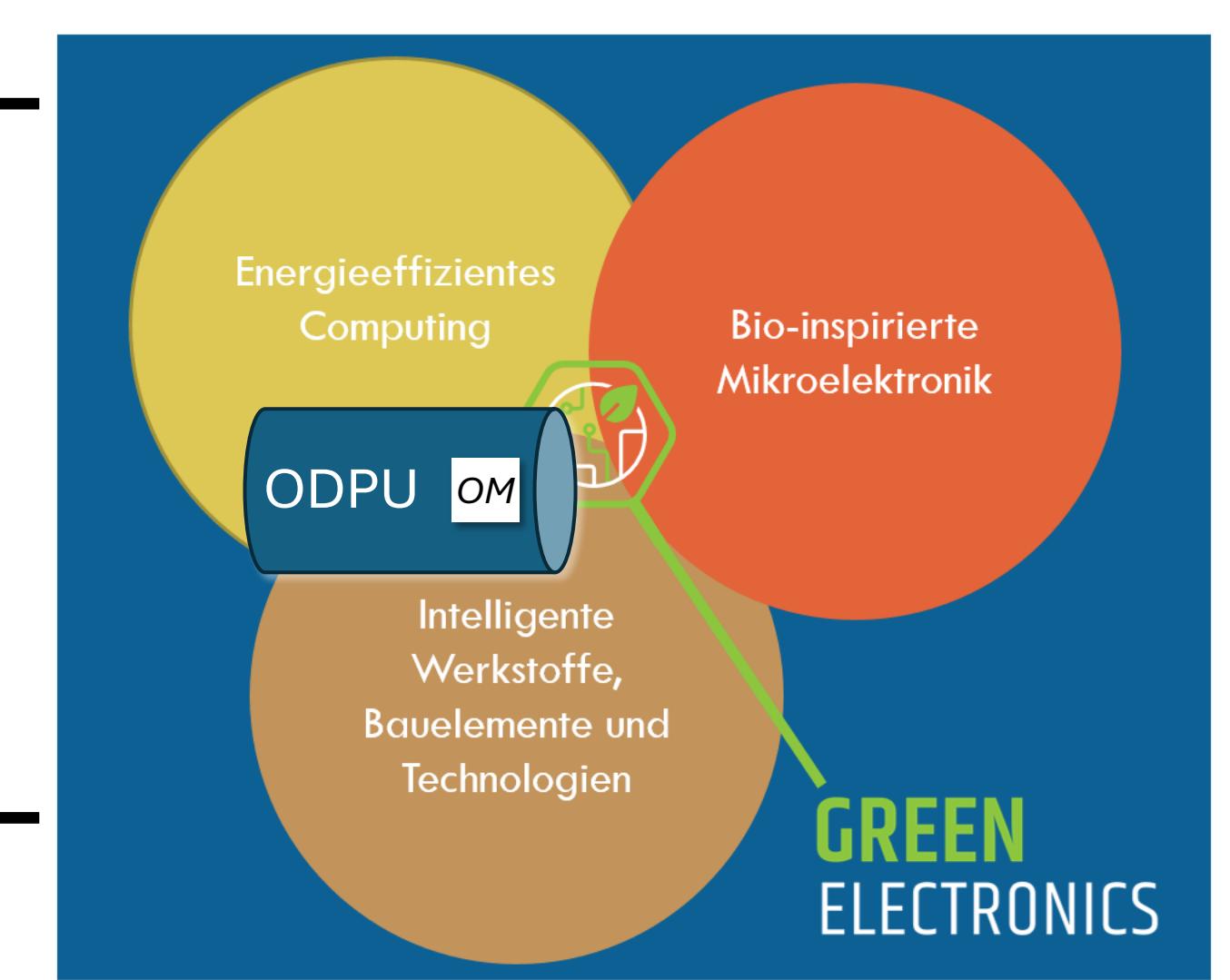


# Optical Computing in Data Processing Applications



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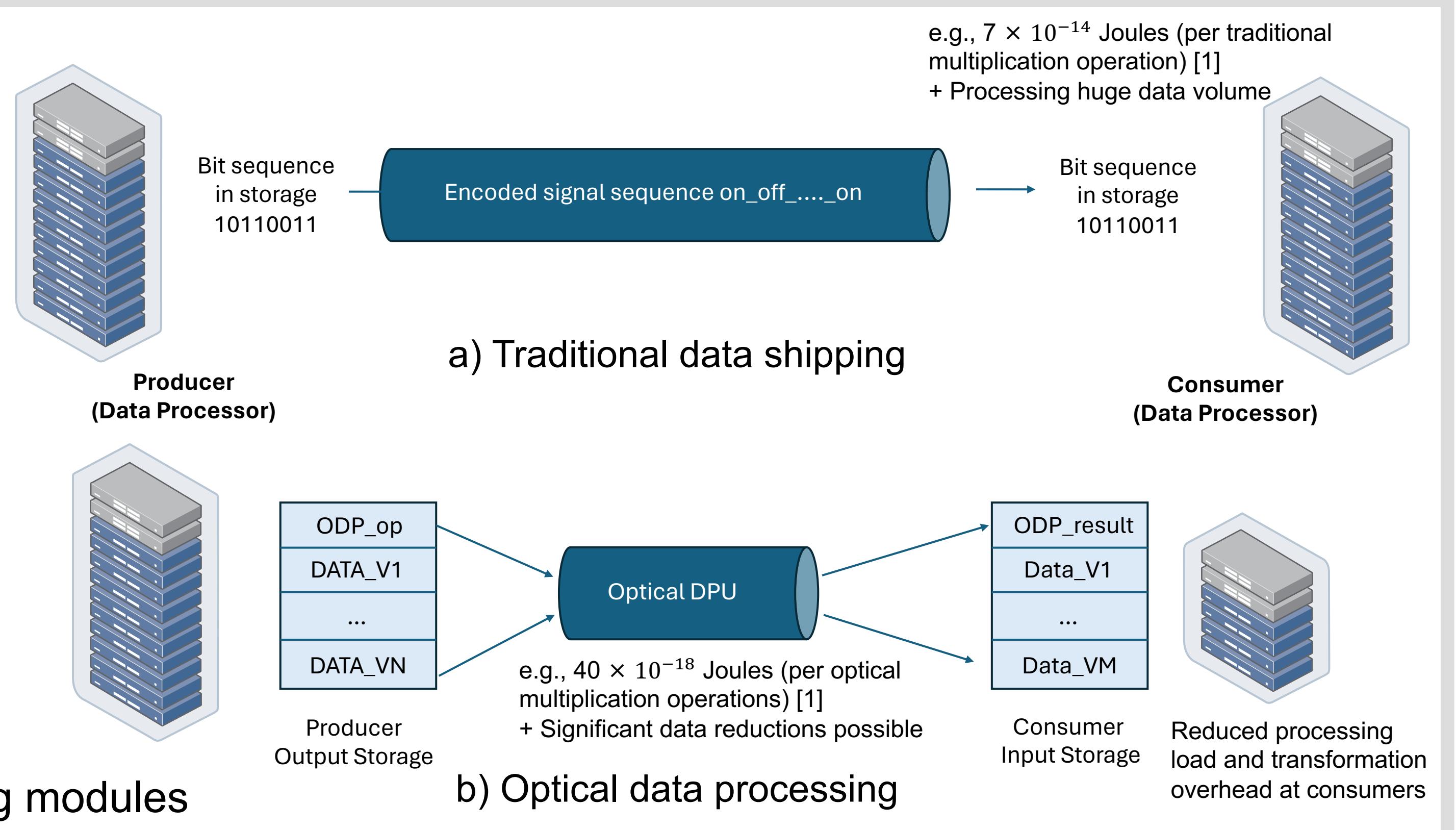
## Motivation / Relevance for ISGE

### Motivation

- Nowadays substantial amount of energy is spent on *Data Processing*
- Data movements is a significant cost factor for energy due to
  - Separation of data movement and processing
  - Overhead in digital / analog transformations
- Optical processing:** huge potential to collocating computation and transfer for higher energy efficiency!
- Passive optical modules (OM)** enable analog optical data processing for data correlation and analytics
- Requires appropriate computational/programming models

### Research Goal

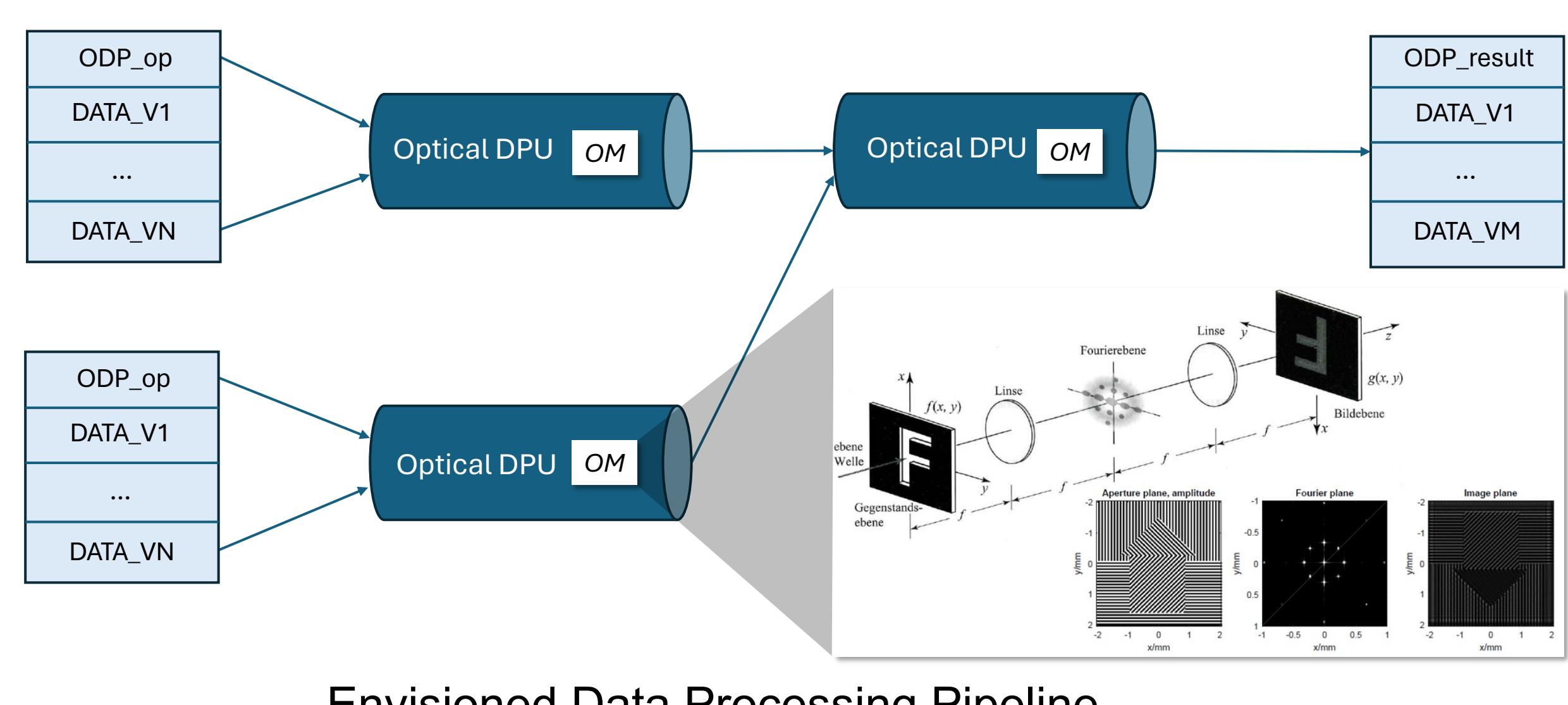
- Research methods for building and composing analog optical data processing modules to enable energy-efficient data processing pipelines



## Approach and Research Questions

### Approach

- Benefit from processing capability of optical communication channels
- Research methods for interconnecting *Optical DPU* to support data processing pipelines



## Work Program

### WP1 Physical Foundations of ODP:

- Assessment of the physical scope
- Integrated ODP modules with transmission capabilities
- Integration possibilities into data processing pipelines

### WP2 ODP Computing Model:

- Performance model of ODP modules
- Optimized numerical interfaces
- Prototyping of limited functionality for experimental demonstration

### Research Questions

- Computational expressiveness:
  - Suitable data processing operations?
- Integration:
  - How fabricating and integrating passive optical modules (OM)?
  - Implication to computing architectures, such as processing pipelines, in-memory computing architectures
  - Technological building blocks, connectors, networks, etc.?
- Programming model:
  - Efficient support for data processing paradigms, e.g., with stream processing or map reduce?
- Energy efficiency:
  - Gain in terms of energy efficiency?
  - What is the influence of properties of specific data processing applications?

## References / Work of the research teams

- [1] M. Yang, Z. Zhong, and M. Ghobadi. **On-Fiber Photonic Computing**. In: Proceedings of the 22nd ACM Workshop on Hot Topics in Networks, HotNets '23, Cambridge, MA, USA: Association for Computing Machinery, 2023, pp. 263–271. isbn: 978-1-4503-979-8-40-070415-4. doi: 10.1145/3626111.3628177.
- [2] A. Vander Lugt. **Optical Signal Processing**. 1st ed. Wiley-Interscience, New York, Mar. 1992. isbn: 978-0471546825.
- [3] J.L. Horner, ed. **Optical Signal Processing**. 1st ed. Academic Press, New York, 1987. isbn: 9780123557605.
- [4] W. Eckert, V. Arriozon, S. Sinzinger, and J. Jahns. **Compact planar-integrated optical correlator for spatially incoherent signals**. In: Applied Optics 39.5 (2000), pp. 759–765. doi: 10.1364/AO.39.000759.
- [5] A. Frömmgen, A. Rizk, T. Erbshäußer, M. Weller, B. Koldehofe, A. Buchmann, and R. Steinmetz. **A Programming Model for Application-defined Multipath TCP Scheduling**. In: Proceedings of the 18th ACM/IFIP/USENIX Middleware Conference. ACM press, 2017, pp. 134–146.
- [6] B. Ottenwälder, B. Koldehofe, K. Rothermel, K. Hong, D. Lillethun, and U. Ramachandran. **MCEP: A Mobility-Aware Complex Event Processing System**. In: ACM Transactions on Internet Technology 14.1 (2014), 6:1–6:24. doi: 10.1145/2633688.
- [7] M. Hofmann, S. Hauguth-Frank, V. Lebedev, O. Ambacher, and S. Sinzinger. **Sapphire-GaN-based planar integrated free-space optical system**. In: Applied Optics 47.16 (June 2008), pp. 2950–2955. doi: 10.1364/AO.47.002950.
- [8] M. Hofmann, Y. Xiao, S. Sherman, U. Gleissner, T. Schmidt, and H. Zappe. **Asymmetric Mach-Zehnder interferometers without an interaction window in polymer foils for refractive index sensing**. In: Applied Optics 55.5 (Feb. 2016), pp. 1124–1131. doi: 10.1364/AO.55.001124.
- [9] D. Fey, W. Erhard, M. Gruber, J. Jahns, H. Bartelt, G. Grimm, L. Hoppe, and S. Sinzinger. **Optical interconnects for neural and reconfigurable VLSI architectures**. In: Proceedings of the IEEE 88.6 (2000), pp. 838–848. doi: 10.1109/5.867697.
- [10] S. Saleh, A. S. Goossens, S. Shu, T. Banerjee and B. Koldehofe. **Analog In-Network Computing through Memristor-based Match-Compute Processing**. To appear in Proceedings of the 43rd IEEE International Conference on Computer Communications, (INFOCOM 2024), IEEE, 2024.

## Work Plan / Funding

### Applied Funding

- One doctoral researcher (4 years)
- 5000€ / Annum travel and publication costs

Work Package	Quartal	24	25	26	27	28							
		3	4	1	2	3	4	1	2	3	4	1	2
WP1: Physical Foundations of ODP													
WP2: ODP Computing Model													
WP3: Adoption to Stream Processing Systems													
WP4: Evaluation													